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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **IJIMA, Takahiro, et al.**

Group Art Unit: **2829** (Expected)

Serial No.: **10/709,912**

Examiner: **Scott B. Geyer** (expected)

Filed: **June 4, 2004**

P.T.O. Confirmation No.: 3911

For: **SEMICONDUCTOR PACKAGE, METHOD OF MANUFACTURING THE  
SAME, AND SEMICONDUCTOR DEVICE**

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 8, 2004

Sir:

Prior to examination on the merits, please amend the above-identified application as  
follows:

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